

# **Dr. T. N. PRABAKAR**

Principal,

Department of Electronics & Communication Engineering,

Oxford Engineering College,

Trichy – 620 009.

## **RESEARCH PROFILE**

### **Areas of Interest:**

VLSI Design, Low Power VLSI, Embedded System Design, Sensors & Transducers, Control Theory, Microprocessors, Linear Integrated Circuits Cryptography.

### **Areas of Research:**

Digital VLSI Design Embedded Systems, Cryptography.

### **Publications [27]:**

- International Journal :06
- IEEE Conferences : 20
- Other Conferences :01

(Publications, citations, h-index and i10-index as provided by GOOGLE Scholar is annexed)

### **Funded Projects [2]:**

- Serving as Investigator in DST project “ Technology enhancement in traditional lock making” along with TNTDPC of CII.
- Serving as CO-Investigator in the UKIERI Project along with Dr.G.Seetharaman, Principal, Oxford Engineering College, Trichy (2015-2017),DST,Rx.40.00 lakhs

### **Patent Filed [1]:**

- Design of Global Asynchronous Controller
- Date of filing: 01 March 2013
- Indian Patent Organization
- Ref NO.596/CHE/2013 in Journal number 09/2013

## **Professional Membership**

Member of ISOI, IEEE, CSI

## **Research Guidance:**

- Ph.D Ongoing
- M.Tech More than 10
- B.E More than 20

## **Professional Up gradation**

### **Events Attended**

- Advanced Refresher Program for VLSI Faculty with Hands on training on Analog-Mixed Signal
- Cadence Tools, Oxford Engineering College, Trichy (2010)
- Embedded System Design Using MICRO-CONTROLLERS, IISC, Bangalore (2010)
- Workshop on Design of High Performance Systems using FPGAs , National Institute of Technology, Tiruchirappalli.(2009)
- Workshop on Telemedicine under TEQIP , National Institute of Technology, Tiruchirappalli (2008)
- Workshop on E-Resources Management for Excellence, Indian Institute of Technology, Madras. (2007)
- Short Term Course on Computer Networks and Distributed Control Systems , B.S. Abdur Rahman Crescent Engineering College, Chennai (2007)
- Workshop on Designing Systems on Programmable Chip(SOPC), National Institute of Technology, Tiruchirappalli.(2007)
- Advanced aspects of VLSI Design Automation , Indian Institute of Technology, Madras (2004)
- Digital VLSI Front , Indian Institute of Technology, Madras .(2004)

### **Events Organized**

- Vedic Mathematics, Oxford Engineering College on 14.09.2011.
- High Speed Wireless Communication, Oxford Engineering College on 23.02.2013.
- Implementing of DSP designs in FPGA using DSP Builder, Oxford Engineering College on 07-09-2012 & 08-09-2012.
- World Space Week, Oxford Engineering College 05-10-2012 & 06-10-2012.
- Design optimization using Xilinx plan Ahead And Partial Reconfiguration, Oxford Engineering College on 16-12-2011 & 17-12-2011

## **Expertise Profile**

### **Software**

- Altera Quartus II, Xilinx ISE, Matlab,
- Android Development Kit,
- Embedded Program Compilers,
- Cadence

## **Hardware**

- Altera FPGAs,
- Xilinx FPGAs
- PIC, Atmel, AVR microcontrollers

## **Other Relevant Information**

- Reviewer for IEEE, Taylor & Francis conferences and journals in the field of VLSI design.
- Ph. D work “INTASYCON” was fabricated as an ASIC Chip under the India Chip Programme 2010 sponsored by DIT, Government of India.
- Served as Project Associate under Dr. G. Uma, Department of ICE, NITT in the MHRD project “Development of GYRO modules” in National Institute of Technology, Trichy.
- Contributed as NAAC Co-ordinator and assisted the institution to get ‘B’ grade.